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REMARKS:

Serial No. 10/672,960, filed 09/26/2003

Attached hereto is a Transmittal of Declaration for filing with the U.S. Patent and Trademark Office. Please acknowledge receipt of this facsimile.

Total Number of Pages (Including This One): SIX (6)

FROM: Jonathan M. Harris, Direct Dial No. (713) 238-8045

DATE: October 14, 2004

CLIENT/MATTER NO. 200302257-2 (2162-12001)

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PATENT APPLICATION

ATTORNEY DOCKET NO. 200302257-2

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s):

Kourosh GHARACHORLOO et al.

Confirmation No.: 9440

Application No.: 10/672,960

Examiner: Unknown

Filing Date:

09/26/2003

Group Art Unit: 2186

Title:

SYSTEM AND METHOD FOR LIMITED FANOUT DAISY CHAINING OF CACHE

INVALIDATION REQUESTS IN A SHARED-MEMORY MULTIPROCESSOR SYSTEM

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Number of pages: 5

Typed Name: Christina L. Paz

Signature:_

Respectfully submitted,

Kourosh GHARACHORLOO et al.

Jonathan M. Harris

Attorney/Agent for Applicant(s)

Reg. No. 44,144

Date: 10/14/2004

Telephone No.: (713) 238-8000

PAGE 2/6 * RCVD AT 10/14/2004 3:59:22 PM [Eastern Daylight Time] * SVR:USPTO-EFXRF-1/3 * DNIS:8729306 * CSID:7132388008 * DURATION (mm-ss):02-58

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Number of pages: 5

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Signature:

Respectfully submitted,

Kourosh GHARACHORLOO et al.

Jonathan M. Harris

Attorney/Agent for Applicant(s)

Reg. No. 44,144

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